

SVF (Software Validation Framework) for OBC and Payloads

The SVF is a complex testing suite designed to test the Control SW (CSW) of residing in the on-board computer of microsatellites or its Payloads/Instruments. It provides means for both integration and validation testing as it allows to test the CSW directly in its target's HW environment connected to SVF via a set of HW interfaces. It is the ideal part for your HW-in-the-loop (HIL) type of testing. In case of missing HW instruments during the development phase, the SVF can emulate the basic functionality of multiple devices that the CSW shall interface.

The modularity of the SVF allows for simple modifications of the modules required by the user. It is possible to create an ideal mix of modules that fit directly to the customer's needs.

The SVF logs and saves all relevant data to auxiliary Data Storage for deep analysis after every test. Those can be run both manually with the interaction between the operator and device under test and autonomously individually or in batches.



SVF FEATURES

- Modular: simple to customize and extend the functionality according to the client
- Supports manual, semi-manual and autonomous testing
- Interaction between the test operator and the system under test
- Easy to read generated Test reports or logs
- SVF-wide Data storage to archive all critical data for later analysis
- HW devices emulation (ADC, RF transceivers, PPS, etc.)
- Supported interfaces: CAN/CANopen, SpaceWire, SPI, UART, Ethernet
- Running on Windows 10 and Linux without any other required dependencies

PRICE

- Call us: +420 284 683 784, or write us: info@esc-aerospace.cz

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